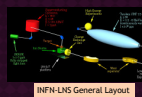


NEW PC-BASED CONTROL FOR THE RF SYSTEM AT INFN-LNS

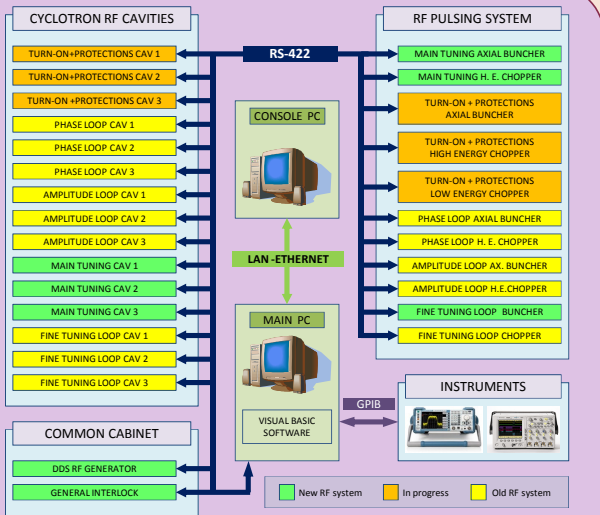
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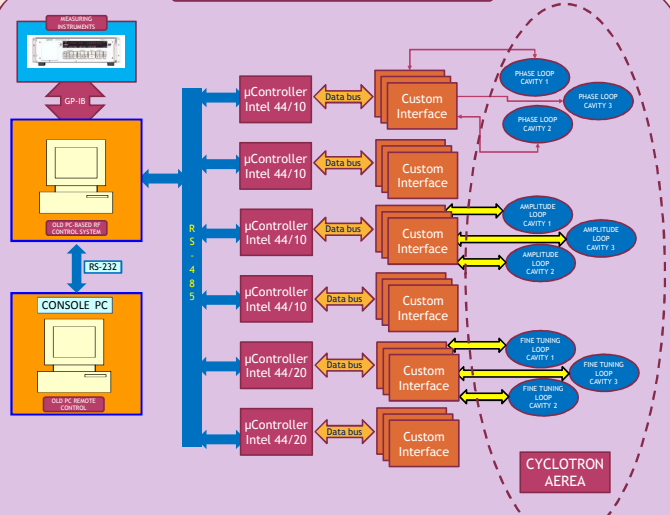
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NEW RF CONTROL SYSTEM ARCHITECTURE



OLD RF CONTROL SYSTEM ARCHITECTURE



RF GENERATOR BASED ON DIRECT DIGITAL SYNTHESIZER

DDS GENERATOR RESOLUTION
The core of the DDS generator is based on the icjup AD9854. The frequency resolution is 48 bit, the system clock is 300 MHz. The amplitude and phase resolutions are related to the bit conversion only, 14 bit for the phase and 12 bit for the amplitude give $\Delta\phi = 2\pi/2^{14}$, $\Delta V = V_{max}/2^{12}$, $\Delta f = f_s - f_{output}$

$\Delta f = 0.394 \mu\text{Hz}$
 $\Delta\phi = 0.02^\circ$
 $\Delta V = 43 \mu\text{V}$

The block diagram shows the DDS generator architecture. It includes a 50MHz Crystal Oscillator and a 50MHz External Oscillator. The DDS1 to DDSn blocks are connected to RF CAV1, RF CAV2, RF CAV3, and RF Auxiliary. The system is controlled via RS422.

Main Tuning System

The main tuning system sets and controls the position of the variable sliding shorts of the three coaxial resonators. The tuning resolution is 0.1 mm in the cyclotron frequency range between 15 and 50 MHz.

The flowchart shows the general RF control system. It starts with the New Turn-on and Protections System, which feeds into the Phase Loop and Amplitude Loop. The Amplitude Loop includes an Amplitude Modulator. The output goes through an RF amplifier (60dBm/250W) and a Directional coupler (79.5dBm/90kW) to the RF 3λ Coaxial Resonator of the K-800 cyclotron. A Fine Tuning Loop (Δf = 40kHz) is also shown.

THE GENERAL CONTROL INTERLOCK SYSTEM

The diagram shows the general control interlock system. It includes sensors for Vacuum Accelerator, Vacuum Liner, Temperature, Water, and RF On/Off Master Switch. These sensors are connected to a PIC18F4550 microcontroller, which outputs to a 10-bit ADC and is connected to the TO DNS RF FNABIE and LCD Status Indicator. The system is controlled via RS-422.

RF devices along the beam line

The cyclotron resonators
- frequency range 15-50 MHz
- 1/2 coaxial resonator, connected at the center by the Dees
- sliding short movement between 0.80-3.5m
- 120° phase difference between the dees, h=2
- Phase stability < 0.1°
- Amplitude stability better than 0.01%

The high energy chopper
- frequency range 4.5-16MHz
- Max 70kW, Vpp 1.5kV
- Qmax = 450 (loaded)
- Repetition time 100-200ns
- 1 LC lumped element
- Inductive coupled
- water cooled coil
- Rmax = 190kHz @ 14kV, 50W

The axial buncher
- Drift tube solution inside the CS yoke
- 1m meter from median plane
- frequency range 15-50 MHz
- Electrode length 41 mm, L=26, L=26, L=26
- Efficiency measured 3.5-4.5
- Extracted beam bunches 1ns FWHM
- Ibeam 1500 A, Pbeam 150 W
- Vacuum environment

The low energy chopper
as beam attenuator
- Frequency 1-10 kHz
- Duty cycle 0-100%
- Vmax 1500 Vmax

The future Chopper-500
- Maximum Peak Voltage 200 kV
- Frequency range 95-110 MHz
- Q= 6000-9000
- IS Chant 450-720 kA
- Power dissipated 27-45 kW
- 150kW
- Coupling capacitive
- Input impedance 50Ω on 3"

AXIAL BUNCHER TUNING AND MATCHING BOX

The diagram shows the axial buncher tuning and matching box. It includes a matching box with three fault-hunter controllers (M1, M2, M3) connected to a μ-Controller. The system is controlled via RS422. A control panel and matching box are also shown.

